



UNIVERSITÀ DEGLI STUDI DI PALERMO

DEPARTMENT	Ingegneria
ACADEMIC YEAR	2020/2021
BACHELOR'S DEGREE (BSC)	COMPUTER ENGINEERING
SUBJECT	ELECTRICAL DEVICES AND CIRCUITS
TYPE OF EDUCATIONAL ACTIVITY	C
AMBIT	10655-Attività formative affini o integrative
CODE	02965
SCIENTIFIC SECTOR(S)	ING-IND/31
HEAD PROFESSOR(S)	ALA GUIDO Professore Ordinario Univ. di PALERMO
OTHER PROFESSOR(S)	
CREDITS	6
INDIVIDUAL STUDY (Hrs)	102
COURSE ACTIVITY (Hrs)	48
PROPAEDEUTICAL SUBJECTS	
MUTUALIZATION	
YEAR	2
TERM (SEMESTER)	1° semester
ATTENDANCE	Not mandatory
EVALUATION	Out of 30
TEACHER OFFICE HOURS	ALA GUIDO Monday 10:00 11:00 ufficio 2022, edificio 9, viale delle Scienze, Palermo

PREREQUISITES	Mandatory: None. Recommended: Knowledge of basic topics of mathematical analysis courses (functions, derivatives, integrals, sequences, series, matrix calculation, complex numbers), analytical geometry (display and analysis functions), General Physics (basics of static electric and magnetic fields).
LEARNING OUTCOMES	<p>D.1: KNOWLEDGE AND UNDERSTANDING SKILLS The student at the end of the course will have acquired the knowledge on the analysis of electrical circuits in adynamic and, sinusoidal steady state at different frequencies. In this context it will have acquired the necessary knowledge of the major methods of resolution of electrical circuits, however complex, and also deal with the solution with the aid of advanced textbooks. This objective is verified during the written test; the possible interview (optional), may be useful in further refinement of the judgment on the achievement of the result.</p> <p>D.2: SKILLS TO APPLY KNOWLEDGE AND UNDERSTANDING The student will be able to analyze and understand the functioning of the components and linear circuits, will be able to apply the knowledge and understanding gained in analyzes of circuits not resolved during lessons and non-complex problems of circuit synthesis using the correct terminology and demonstrating a professional approach. This objective is verified during the written test; the possible interview (optional), may be useful in further refinement of the judgment on the achievement of the result.</p> <p>D.3: JUDGING AUTONOMY The student will be able to assess the implications and results of the study of linear circuits, establishing the necessary links with the analysis and mathematical physics, and will have acquired the skills necessary to independently assess the implications of the topics discussed with the topics of the rest of the course. This objective is verified during the written test; the possible interview (optional), may be useful in further refinement of the judgment on the achievement of the result.</p> <p>D.4: ENABLE COMMUNICATION The student will acquire the ability to communicate and express issues concerning the object of the course with adequate technical terminology, about the problems related to electrical circuits and express and offer ideas and original solutions to the problems of analysis and synthesis of circuits communicating with specialists or not-specialists. This objective is verified during the written test; the possible interview (optional), may be useful in further refinement of the judgment on the achievement of the result.</p> <p>D.5: LEARNING CAPACITIES The student will have learned the interactions between the principles and methods of circuit theory and analysis and synthesis problems of linear electrical circuits, meanwhile acquiring the skills necessary to pursue their own path with greater autonomy training. This objective is verified during the written test; the possible interview (optional), may be useful in further refinement of the judgment on the achievement of the result.</p>
ASSESSMENT METHODS	<p>Written test. An interview (at the student's request) can be useful in improving the result of the written test.</p> <p>The written test consists in solving circuits and problems similar to those carried out during the exercises. Generally the exercises to be performed are three divided by macro issues such circuits in steady state and circuits in sinusoidal steady state. The written test has a duration of between 2 and 3 hours depending on the difficulty level. To take the oral test (at the student's request) you must pass the written exam with a mark equal to or greater than 18/30. In the written test are evaluated: - mastery and ability to use the basic circuit analysis concepts; - Ability to argue and analyze your choices. The oral test consists of an interview which concerns possible mistakes made in the written test and open-ended questions on the entire course program. In the oral examination are evaluated: knowledge and understanding of the course content and the ability to apply these skills to problems and applications in their areas of the course and / or related to it; of language and clarity of presentation and argumentation; capacity 'to connect and build upon their skills, and build and make judgments in disciplinary and / or interdisciplinary contexts. The final mark takes into account so prevalent of the result of the written test from which it's off to the determination of the final grade.</p> <p>EVALUATION CRITERIA</p> <p>For each test, the voting attribution depends on the overall level of achievements. The elements that contribute to the vote formation are attributable to the following diagram (see the context of expected learning outcomes, D.1-D.5 descriptors).</p> <p>- 28-30 / 30 cum laude D.1 / D.2: full mastery of contents; absence of errors; correcting inaccuracies or integration of the responses independently; sound and rigorous approach of the problems; complete solutions, correct and effective; elements of originality. D.3 / D4 / D5: Effective knowledge reproduction, autonomy and coherence orienting or comment on disciplinary / interdisciplinary contexts; excellent clarity, articulate arguments; full property of language.</p> <p>- 24-27 D.1 / D.2: good command of the contents; a few minor errors / omissions, corrections / additions partially guided; good set of problems, substantially correct solutions. D.3 / D4 / D5: good consistency in connecting the</p>

	<p>concepts and orienting in disciplines or related to them; good clarity in, correct properties of language.</p> <p>- 18-23 D.1 / D.2: sufficient knowledge of the content, acceptable approach to the problems, being adequate solutions; limited autonomy, errors / omissions are not serious; D.3 / D4 / D5: orienting consistency and connect concepts in disciplinary matters, although so uncertain and driven; sufficient property of language, acceptable exposure.</p> <p>- Less than 18 (not attributed vote) D.1-D.5: learning outcomes inadequate.</p>
EDUCATIONAL OBJECTIVES	<ul style="list-style-type: none"> • Being able, through knowledge of the behavior of the main circuit elements and the main circuit analysis methods, to solve still complex linear circuits in adynamic and sinusoidal steady state. • Evaluate the behavior of the resonant circuit and not through the analysis in the frequency domain and addressing simple problems synthesis of analog filters. • Know and derive the parametric features of biporta circuits also interconnected.
TEACHING METHODS	<p>Lectures, classroom exercises.</p> <p>Activities' they are organized in order to facilitate the achievement of the expected learning outcomes. In detail, the contents of the course are offered through frontal lectures and guided exercises. The lectures are supplemented by exercises during which you can gradually apply the theoretical principles to the solution of electrical circuits, thus stimulating 'the development of the capacity' of application of knowledge and skills' acquired.</p> <p>During the lessons, in part dialogues and interactive, as well as' on the occasion of the exercises, students are expected to analyze the proposals issues critically, thus developing 'its capacity' of analysis and independent judgment. Simultaneously the student and 'incentive to develop communicative skills, argumentation and properties' of language, through the various opportunities for interaction and dialogue with the teacher and other students. All the activities of the course ultimately contribute to the development of learning skills, through the revision of the knowledge acquired, the references to real and interdisciplinary applications and the urge to face the resolution of circuits not covered during the course independently.</p>
SUGGESTED BIBLIOGRAPHY	<p>References</p> <ul style="list-style-type: none"> •R Perfetti: "Circuiti elettrici" - Zanichelli, 2012 •Exercises proposed by the professor (link at unipa students home page). •M. Repetto, S. Leva: "Elettrotecnica - Elementi di teoria ed esercizi" – CittaStudi Edizioni, II Ed. 2018. •A. R. Hambley: "Elettrotecnica" – Pearson - Prentice Hall, IV Ed. 2013. <p>---</p> <ul style="list-style-type: none"> •Bagatin, Chitarin, Desideri, Dughiero, Gnesotto, Guarnieri, Maschio: Esercizi di Elettrotecnica - reti elettriche - Societa' Ed. Esculapio, 2013. •Geri, Maccioni: Raccolta di esercizi d'esame di Elettrotecnica - Societa' Ed. Esculapio, 2013. •Liberatore, S. Manetti, M.C. Piccirilli, A. Reatti: "Circuiti elettrici ed elettronici - Esercizi commentati e risolti" – Progetto Leonardo, Bologna, 2003.

SYLLABUS

Hrs	Frontal teaching
3	Introduction to the course. Lumped circuits. Limits and validity of the model. Electrical values, principles of Kirchhoff. Topological equations. One-port and multi-ports circuits.
4	Electrical circuit theorems. Substitution theorem. Superposition theorem. Thevenin's theorem and Norton. Millmann theorem.
3	One-port circuit components. Parallel series connections. Voltage and current dividers. Star-delta transformation, and vice versa. Thevenin and Norton equivalent resistive circuits.
5	Analysis of linear electrical circuits in steady state. systematic methods for troubleshooting electrical circuits. Source transformation. Circuit analysis based on nodes and on meshes.
6	Analysis of linear electrical circuits in sinusoidal steady state. Phasor. Kirchhoff's laws with phasors. Impedance and admittance. Solution of circuits in sinusoidal regime with phasors. Power in AC circuits. RMS values. Power factor correction. Maximum power transfer theorem.
6	Parallel RLC circuit series in sinusoidal steady state. Resonance. Circuit frequency response. Transfer functions and passive filters.
5	Two-port linear circuits in steady state and in sinusoidal steady state
Hrs	Practice
2	Lumped circuits. Limits and validity of the circuit model. Electrical values, Principles of Kirchhoff. Topological equations. One-port and multi-ports circuits.
3	Theorems of electrical circuits. Substitution theorem. Superposition theorem. Theorems of Thevenin and Norton. Millmann Theorem.

Hrs	Practice
3	One-port circuit elements. Parallel series connections. Voltage and current dividers. Star-delta transformation, and vice versa. Thevenin and Norton equivalent resistive circuits.
2	Analysis of linear electrical circuits in steady state. systematic methods for troubleshooting electrical circuits. Source transformation. Circuits analysis based on nodes and on meshes.
2	Analysis of linear electrical circuits in sinusoidal steady state. Phasor. Kirchhoff's laws with phasors. Impedance and admittance. Solution of circuits in sinusoidal regime with phasors. Power in AC circuits. RMS values. Power factor correction. Maximum power transfer theorem.
2	Parallel RLC circuit series in sinusoidal steady state. Resonance. Circuit frequency response. Transfer functions and passive filters.
2	Two-port linear circuits in steady state and sinusoidal steady state.